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Lecture #1 2 Introduction to digital
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Key concepts needed to be a good
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Models that allow reasoning about circuit behavior Allow analysis and optimization of the circuit's performance, power, cost, etc.

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Prof. Rabaey has made high-impact contributions to a number of fields, including advanced wireless systems, low power integrated circuits, sensor networks, and ubiquitous computing. His current interests include the conception of the next-generation integrated wireless systems over a broad range of applications, as well as exploring the

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Solution The lowest output resistance is obtained when all inputs are equal to

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zero. Each of the pMOS have the same width, so all of them have the same resistance. The worst case resistance happens when only one of the inputs (A, B, C or D) is equal to 0 while all the rest are equal to 1.

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